



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Terletzki, *et al.*

Docket No.: 00P7882US

Serial No.: 09/659,872

Art Unit: 2816

Filed: 9/13/2000

Examiner: Nguyen, Minh T.

For: Level-Shifting Circuitry Having "High" Output Impedance During Disable Mode

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Respectfully submitted,


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